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PATENT
Atty. Dkt. No. NVDA/P002824

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re Application of:

Howard et al.

Patent No.: 7,174,432 B2

Issued:

February 6, 2007

Filed:

August 19, 2003

For:

ASYNCHRONOUS, INDEPENDENT AND MULTIPLE PROCESS SHARED MEMORY

SYSTEM IN AN ADAPTIVE

COMPUTING ARCHITECTURE

Certificate of Correction Branch Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 Serial No.: 10/644,621

Group Art Unit: 2187

Examiner: Kimberly N. McLean Mayo

CERTIFICATE OF MAILING 37 CFR 1.8

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4667

Signature

REQUEST FOR CERTIFICATE OF CORRECTION

Dear Sir:

Attached is a Certificate of Correction for correcting errors made in the claims of the printed patent.

Applicant submits that the errors mentioned above were not made by the applicant, but were made during the printing of the patent.

Certificate

APR 1 1 2007

of Correction

Respectfully submitted,

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UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO: 7,174,432 B2

Page 1 of 1

APPLICATION NO.: 10/644,621

DATED: February 6, 2007

INVENTOR(S): Howard et al.

It is certified that error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

In column 34, line 30, replace "forming" with --performing--

In column 37, line 17, replace "full when" with -full and when--

MAILING ADDRESS OF SENDER (Please do not use customer number below):

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